

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Wadgi W. Abadeer, et al. **Examiner:** Unassigned

Serial No.: To Be Assigned **Art Unit:** Unassigned

Filed: Herewith **Docket:** BUR920000082US2(13647A)

For: WAFER LEVEL SYSTEM FOR
PRODUCING BURN-IN/SCREEN,
AND RELIABILITY EVALUATIONS
TO BE PERFORMED ON ALL CHIPS
SIMULTANEOUSLY WITHOUT ANY
WAFER CONTACTING

Dated: March 16, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Applicants respectfully request that the following amendments be entered into this application concurrently with the filing thereof in the U.S. Patent and Trademark Office:

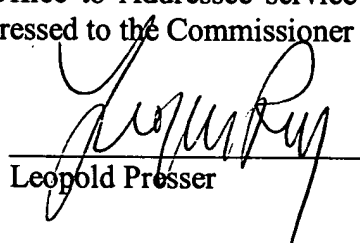
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Date of Deposit: March 16, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service Express Mail Post Office to Addressee service under 37 C.F.R. §1.10 on the date indicated above and is addressed to the Commissioner of Patents and Trademarks, Alexandria, VA 22313-1450.

Dated: March 16, 2004


Leopold Presser